AMENDMENTS TO THE SPECIFICATION

Page 12

Replace the paragraph commencing at line 18 with the following amended paragraph:

Therefore, the test interpolation data ~~TD1[T1] - TD1[T4]~~ TD2[T1] - TD2[T4] is calculated as follows;

$$TD2[T1] = (T1C + T1D) / 2 = (255 + 255) / 2 = 255$$

$$TD2[T2] = (T2C + T2D) / 2 = (255 + 0) / 2 = 127.5$$

$$TD2[T3] = (T3C + T3D) / 2 = (255 + 0) / 2 = 127.5$$

$$TD2[T4] = (T4C + T4D) / 2 = (0 + 0) / 2 = 0$$

Page 19

Replace the paragraph commencing at line 11 with the following amended paragraph:

Fig. 19 is a table showing values of the evaluation data S1, S2 and S3, which are calculated for the left/right averaging interpolation circuit 5, the rightward up averaging interpolation circuit 6, and the leftward up averaging interpolation circuit 7. As illustrated in Fig. 19, the evaluation

data S3 of the leftward up averaging interpolation circuit 7 has a smallest value. As described

above, the smaller the difference between the test interpolation data and actual image data is, the

lower the evaluation data becomes. Because the original image data illustrated in Fig. 16 (a) has

a ~~rightward~~ leftward up outline, the leftward up averaging interpolation circuit 7 is estimated to

have the highest interpolation aptitude. The evaluation data represented in Fig. 19 also indicates

that the leftward up averaging interpolation circuit 7 has the highest interpolation aptitude.